ZX 12864P-38DPSWSN

June 25, 2008
Version 1.0

RECORDS OF REVISION

DATE	REVISED NO.	REVISED DESCRIPTIONS	PREPARED	CHECKED	APPROVED
June 25, 2008	1.00	FIRST ISSUE	YNN		

CONTENTS

1. FEATURES：

ITEM	STANDARD VALUE	UNIT
Display Type	128*64 dots	-
LCD Type	FSTN/Positive Transflective	-
LCD Duty	1/64	-
Viewing Direction	6:00	
Backlight Type	WHITE SIDE LED Backlight	-
Interface	8-BIT MPU（6800/8080）	-
Driver IC	S6B0724	-
LCD Bias	1/9BIAS	-
Module Dimension	71.8(W) X 52.7(H) X 6.3(T)	mm
Effective Display Area	60.775 (W) X32.935(H)	mm
Dot Size	0.45(W) X 0.49(H)	mm
Dot Pitch	0.475W) X 0.515(H)	mm

2. ABSOLUTE MAXIMUM RATING

ITEM	SYMBOL	CONDITION	STANDARD VALUE			UNIT
			MIN	TYP	MAX	
POWER SUPPLY FOR LOGIC	VDD	Ta=25℃	-0.3	—	7.0	V
INPUT VOLTAGE	VIN	Ta=25℃	-0.3	—	VDD+0.3	V
Module OPERATION TEMPERATURE	TOPR	---	-20	—	+70	℃
Module STORAGE TEMPERATURE	TSTG	---	- 30	—	+80	℃
Storage Humidity	H_D	Ta < 40 °C	-		90	%RH

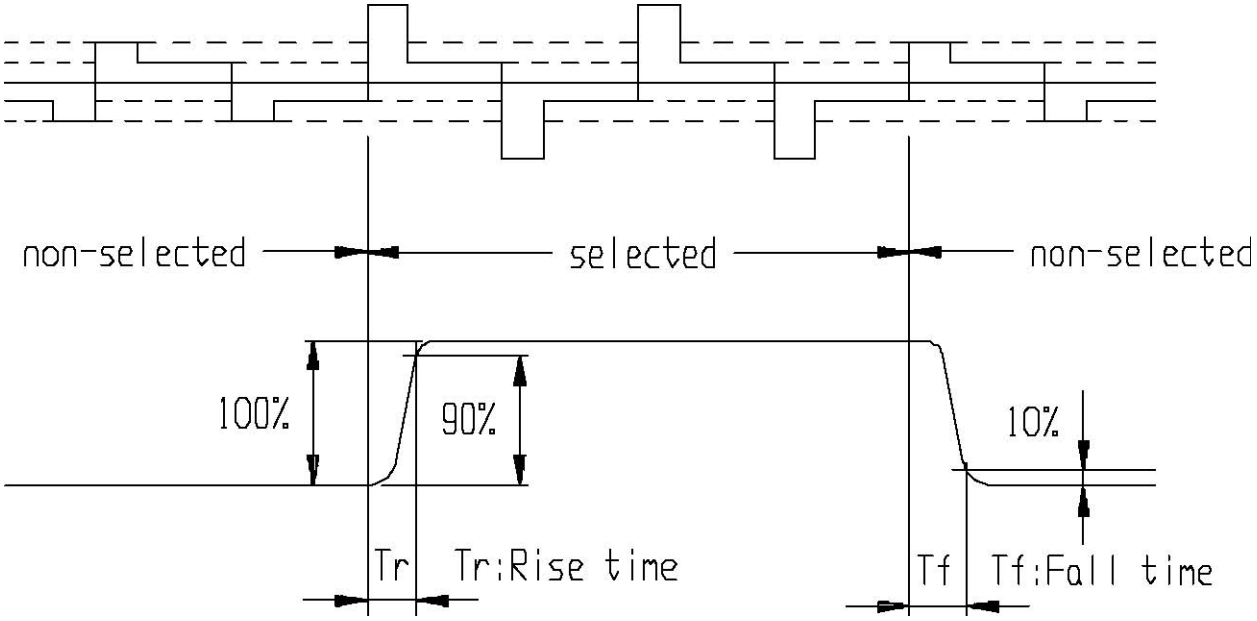
3. ELECTRICAL CHARACTERISTICS

ITEM	SYMBOL	CONDITION	MIN	TYP	MAX	UNIT
Supply Voltage (logic)	VDD-VSS	- Ta= +25℃	-	3.0	-	V
Supply Voltage (LCD)	VDD-V0	Ta= +25℃	-	9.0	-	V
Input signal voltage	V-IH	“H” level	0.8VDD	-	0.2VDD	V
	V-IL	“L” level	-0.3	-	0.8	V
Output signal voltage	V-OH	“H” leve	VDD-0.6	-	VDD	V
	VOL	“L” level	0	-	GND+0.6	V
Supply Current (logic)	IDD	VDD=3.0V	-	-	-	mA
Backlight Voltage	V-BL	-	2.9	3.0	3.3	V
Backlight Current	I-BL	-	-	-		mA

4. OPTICAL CHARACTERISTICS

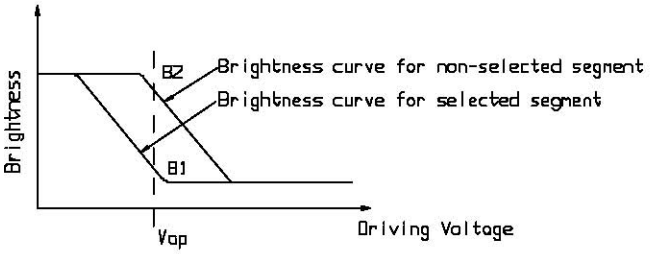
Item	Symbol	Condition	Min	Typ	Max	Unit	Remarks	Note
Response Time	Tr	-	-	110	220	ms	-	1
	Tf	-	-	260	520	ms	-	1
Contrast Ratio	Cr	-	-	3	-	-	-	2
Viewing Angle Range	θ	Cr≥ 2	-	-	30	deg	Ø= 90	3
			-	-	30	deg	Ø = 270	3
			15	-	105	deg	Ø = 0	3
			-	-	-	deg	Ø = 180	3

Note 1. Definition of response time

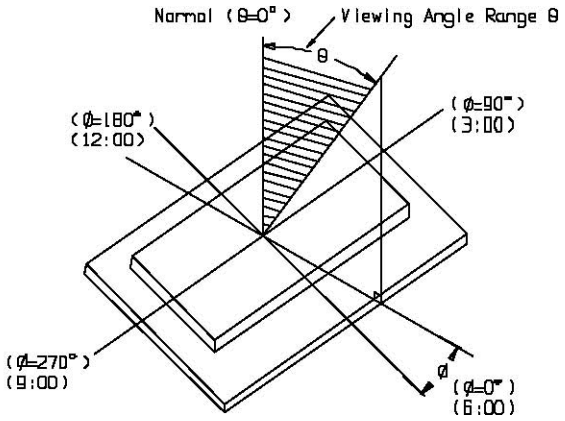


Note 2. Definition of Contrast Ratio 'Cr'

$$Cr = \frac{\text{Brightness of non-selected segment(B2)}}{\text{Brightness of selected segment(B1)}}$$

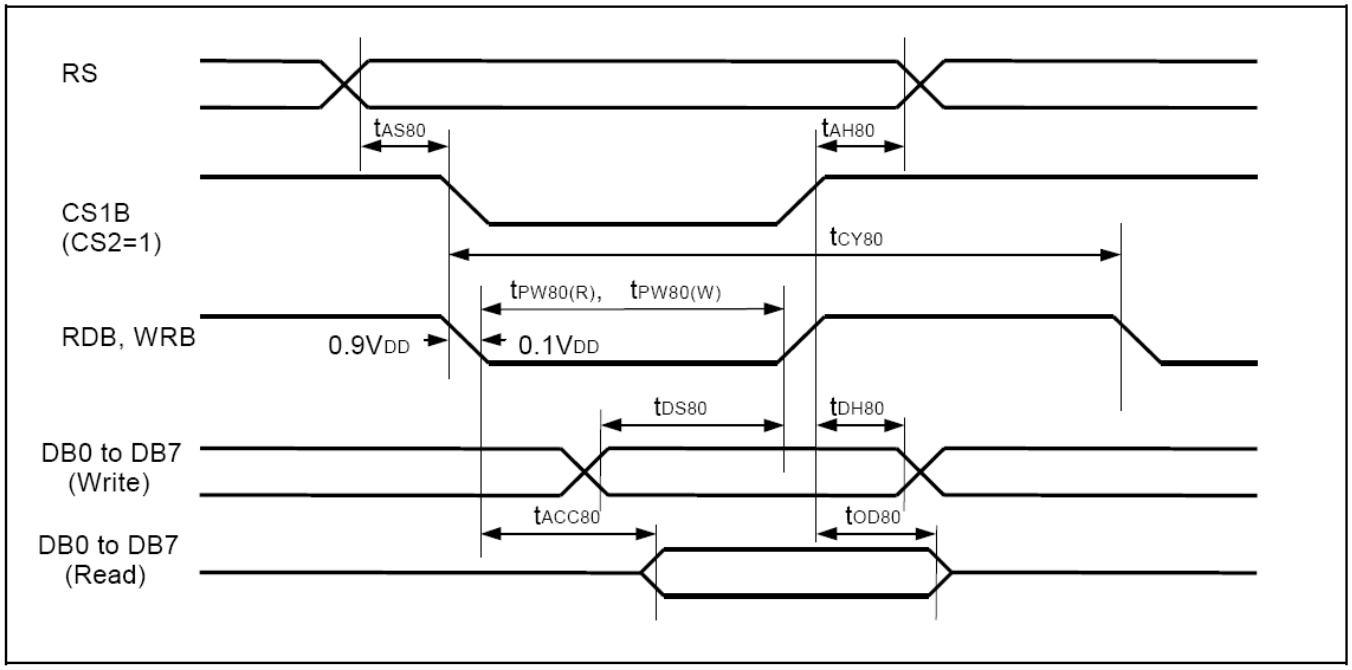


Note 3. Definition of Viewing Angle Range 'q'



5. TIMING CHARACTERISTICS

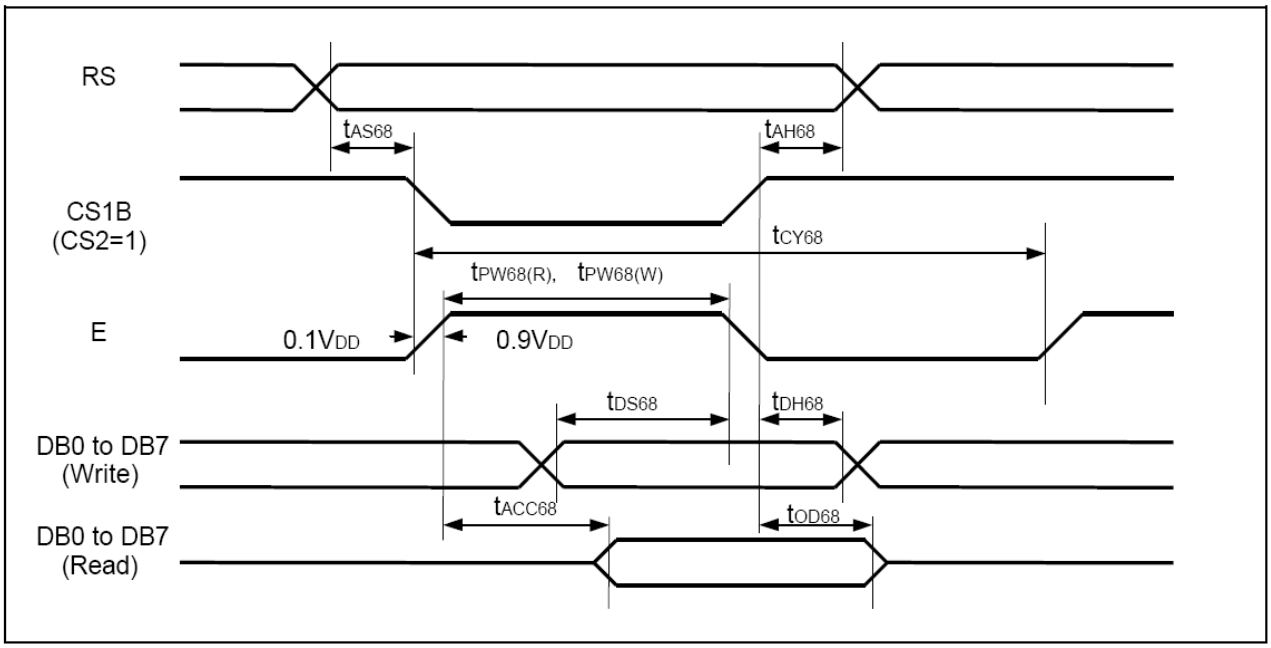
Read / Write Characteristics (8080-series MPU)



(V_{DD} = 2.4 to 3.6V, Ta = -40 to +85°C)

Item	Signal	Symbol	Min.	Typ.	Max.	Unit	Remark
Address setup time Address hold time	RS	t_{AS80} t_{AH80}	0 0	-	-	ns	
System cycle time	RS	t_{CY80}	300	-	-	ns	
Pulse width (WRB)	RW_WRB	$t_{PW80(W)}$	60	-	-	ns	
Pulse width (RDB)	E_RDB	$t_{PW80(R)}$	60	-	-	ns	
Data setup time Data hold time	DB7 to DB0	t_{DS80} t_{DH80}	40 15	-	-	ns	
Read access time Output disable time		t_{ACC80} t_{OD80}	- 10	-	140 100	ns	C_L = 100 pF

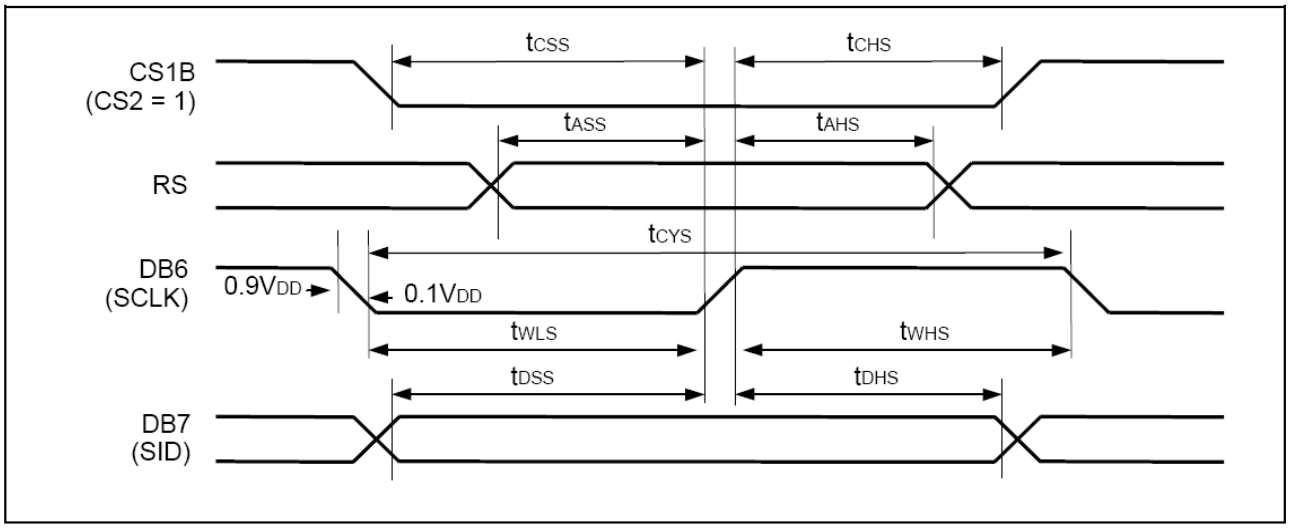
Read / Write Characteristics (6800-series Microprocessor)



(VDD = 2.4 to 3.6V, Ta = -40 to +85°C)

Item		Signal	Symbol	Min.	Typ.	Max.	Unit	Remark
Address setup time Address hold time		RS	tAS68 tAH68	0 0	-	-	ns	
System cycle time		RS	tCY68	300	-	-	ns	
Data setup time Data hold time		DB7 to DB0	tDS68 tDH68	40 15	-	-	ns	
Access time Output disable time			tACC68 tOD68	- 10	-	140 100	ns	CL = 100 pF
Enable pulse width	Read Write	E_RDB	tPW68(R) tPW68(W)	120 60	-	-	-	

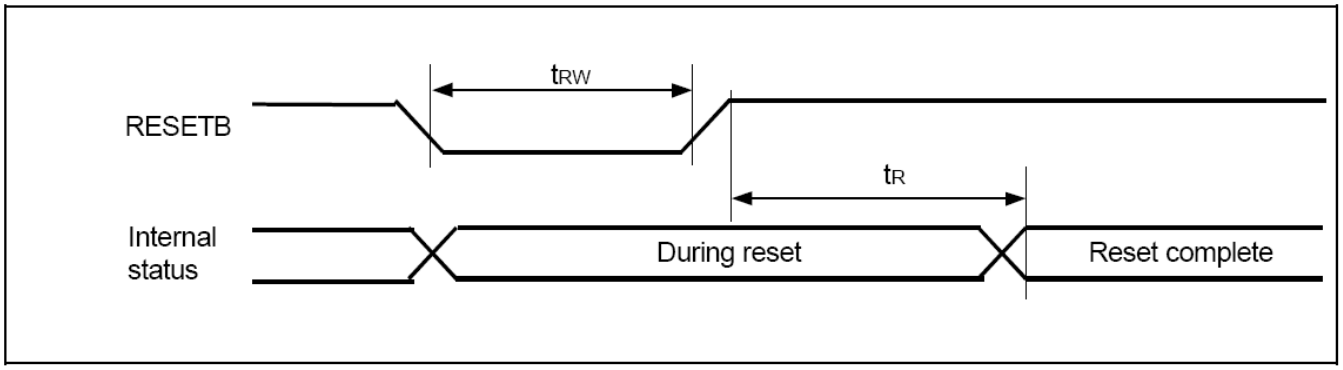
Serial Interface Characteristics



(V_{DD} = 2.4 to 3.6V, Ta = -40 to +85°C)

Item	Signal	Symbol	Min.	Typ.	Max.	Unit	Remark
Serial clock cycle SCLK high pulse width SCLK low pulse width	DB6 (SCLK)	t_{CYS} t_{WHS} t_{WLS}	250 100 100	- - -	- - -	ns	
Address setup time Address hold time	RS	t_{ASS} t_{AHS}	150 150	- -	- -	ns	
Data setup time Data hold time	DB7 (SID)	t_{DSS} t_{DHS}	100 100	- -	- -	ns	
CS1B setup time CS1B hold time	CS1B	t_{CSS} t_{CHS}	150 150	- -	- -	ns	

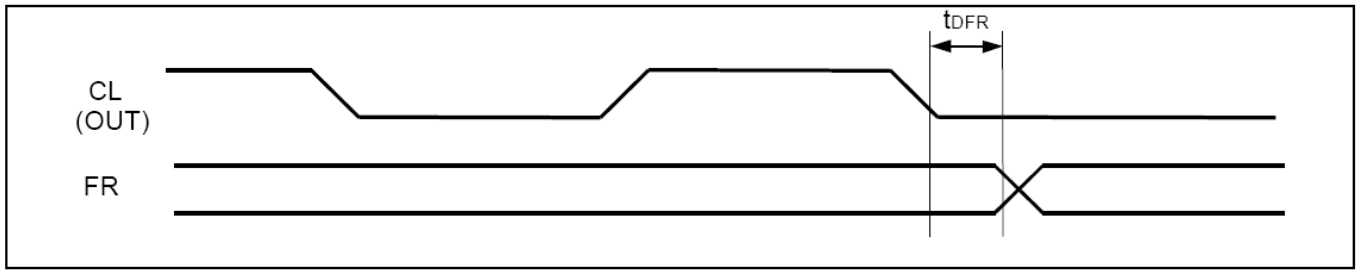
Reset Input Timing



(V_{DD} = 2.4 to 3.6V, Ta = -40 to +85°C)

Item	Signal	Symbol	Min.	Typ.	Max.	Unit	Remark
Reset low pulse width	RESETB	t_{RW}	1.0	-	-	μs	
Reset time	-	t_R	-	-	1.0	μs	

Display Control Output Timing



(V_{DD} = 2.4 to 3.6V, Ta = -40 to +85°C)

Item	Signal	Symbol	Min.	Typ.	Max.	Unit	Remark
FR delay time	FR	t_{DFR}	-	20	80	ns	C_L = 50 pF

6. INSTRUCTION DESCRIPTION

×: Don't care

Instruction	RS	RW	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0	Description
Display ON / OFF	0	0	1	0	1	0	1	1	1	DON	Turn on/off LCD panel When DON = 0: display OFF When DON = 1: display ON
Initial display line	0	0	0	1	ST5	ST4	ST3	ST2	ST1	ST0	Specify DDRAM line for COM0
Set page address	0	0	1	0	1	1	P3	P2	P1	P0	Set page address
Set column address MSB	0	0	0	0	0	1	Y7	Y6	Y5	Y4	Set column address MSB
Set column address LSB	0	0	0	0	0	0	Y3	Y2	Y1	Y0	Set column address LSB
Read status	0	1	BUSY	ADC	ONOFF	RESETB	0	0	0	0	Read the internal status
Write display data	1	0	Write data								Write data into DDRAM
Read display data	1	1	Read data								Read data from DDRAM
ADC select	0	0	1	0	1	0	0	0	0	ADC	Select SEG output direction When ADC = 0: normal direction (SEG0→SEG131) When ADC = 1: reverse direction (SEG131→SEG0)
Reverse display ON / OFF	0	0	1	0	1	0	0	1	1	REV	Select normal / reverse display When REV = 0: normal display When REV = 1: reverse display
Entire display ON / OFF	0	0	1	0	1	0	0	1	0	EON	Select normal/entire display ON When EON = 0: normal display. When EON = 1: entire display ON
LCD bias select	0	0	1	0	1	0	0	0	1	BIAS	Select LCD bias
Set modify-read	0	0	1	1	1	0	0	0	0	0	Set modify-read mode
Reset modify-read	0	0	1	1	1	0	1	1	1	0	release modify-read mode
Reset	0	0	1	1	1	0	0	0	1	0	Initialize the internal functions
SHL select	0	0	1	1	0	0	SHL	×	×	×	Select COM output direction When SHL = 0: normal direction (COM0→COM63) When SHL = 1: reverse direction (COM63→COM0)
Power control	0	0	0	0	1	0	1	VC	VR	VF	Control power circuit operation
Regulator resistor select	0	0	0	0	1	0	0	R2	R1	R0	Select internal resistance ratio of the regulator resistor
Set reference voltage mode	0	0	1	0	0	0	0	0	0	1	Set reference voltage mode
Set reference voltage register	0	0	×	×	SV5	SV4	SV3	SV2	SV1	SV0	Set reference voltage register
Set static indicator mode	0	0	1	0	1	0	1	1	0	SM	Set static indicator mode
Set static indicator register	0	0	×	×	×	×	×	×	S1	S0	Set static indicator register
Power save	-	-	-	-	-	-	-	-	-	-	Compound Instruction of display OFF and entire display ON

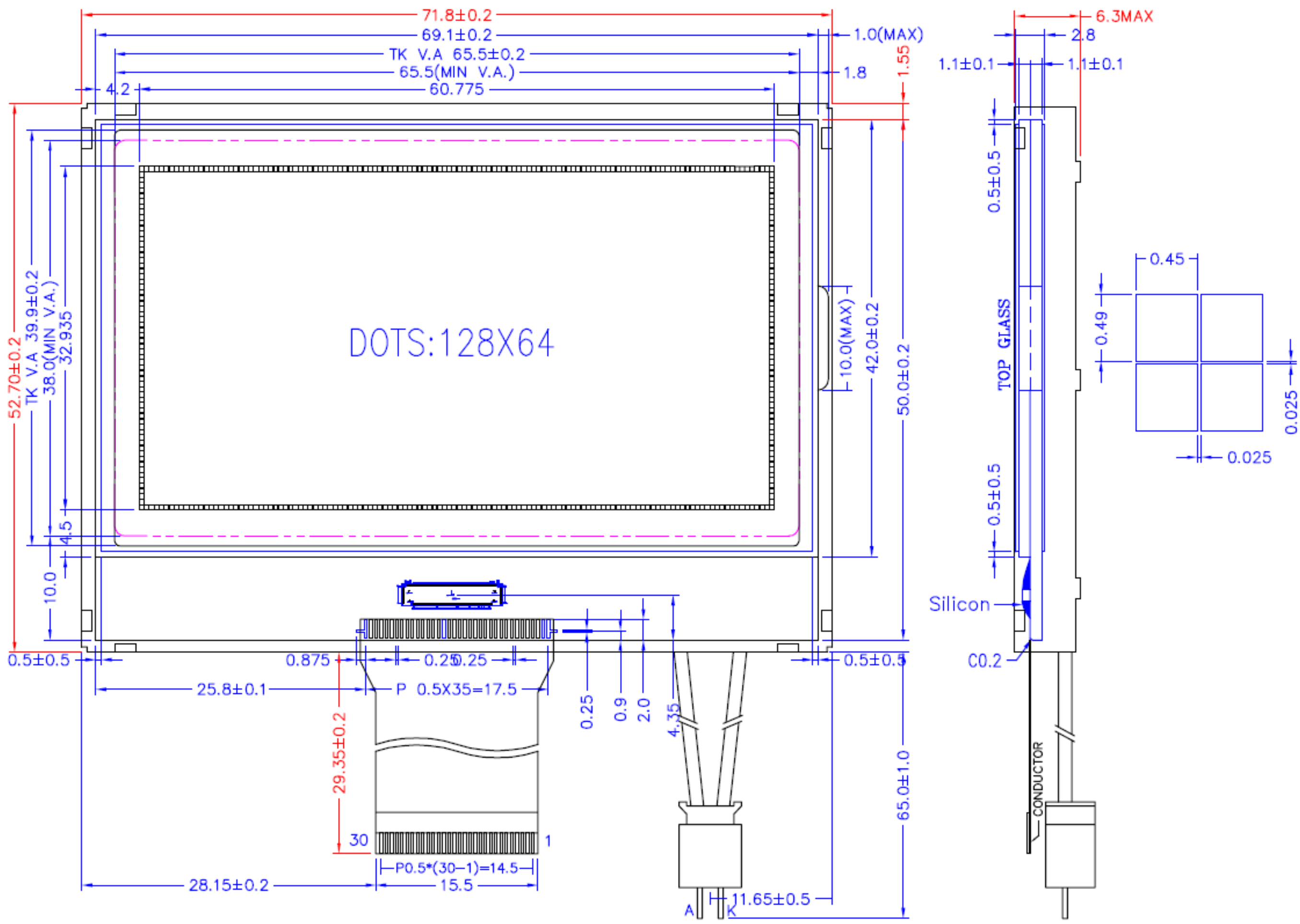
NOP	0	0	1	1	1	0	0	0	1	1	*Non-Operation command*
Test Instruction_1	0	0	1	1	1	1	×	×	×	×	*Don't use this instruction*
Test Instruction_2	0	0	1	0	0	1	×	×	×	×	*Don't use this instruction*

7. PIN ASSIGNMENT

<table>
<tr><th>PIN NO.</th><th>NAME</th><th>I/O</th><th>FUNCTION</th></tr>
<tr><td>1</td><td>/CS1</td><td>I</td><td>Chip select input pin</td></tr>
<tr><td>2</td><td>/RES</td><td>I</td><td>Reset signal</td></tr>
<tr><td>3</td><td>A0</td><td>I</td><td>Register select input pin
A0=H:D0~D7 are display data
A0=L:D0~D7 are control data</td></tr>
<tr><td>4</td><td>/WR</td><td>I</td><td>Write signal</td></tr>
<tr><td>5</td><td>/RD</td><td>I</td><td>Read signal</td></tr>
<tr><td>6</td><td>D0</td><td>I/O</td><td>Data bus 0</td></tr>
<tr><td>7</td><td>D1</td><td>I/O</td><td>Data bus 1</td></tr>
<tr><td>8</td><td>D2</td><td>I/O</td><td>Data bus 2</td></tr>
<tr><td>9</td><td>D3</td><td>I/O</td><td>Data bus 3</td></tr>
<tr><td>10</td><td>D4</td><td>I/O</td><td>Data bus 4</td></tr>
<tr><td>11</td><td>D5</td><td>I/O</td><td>Data bus 5</td></tr>
<tr><td>12</td><td>D6</td><td>I/O</td><td>Data bus 6</td></tr>
<tr><td>13</td><td>D7</td><td>I/O</td><td>Data bus 7</td></tr>
<tr><td>14</td><td>VDD</td><td>I</td><td>Power supply for logic(+3.0V)</td></tr>
<tr><td>15</td><td>VSS</td><td>I</td><td>Ground(0V)</td></tr>
<tr><td>16</td><td>VOUT</td><td>O</td><td>DC-DC output voltage</td></tr>
<tr><td>17</td><td>CAP3+</td><td>I</td><td>Capacitor 3 positive connection pin for voltage converter</td></tr>
<tr><td>18</td><td>CAP1-</td><td>I</td><td>Capacitor 1 negative connection pin for voltage converter</td></tr>
<tr><td>19</td><td>CAP1+</td><td>I</td><td>Capacitor 1 positive connection pin for voltage converter</td></tr>
<tr><td>20</td><td>CAP2+</td><td>I</td><td>Capacitor 2 positive connection pin for voltage converter</td></tr>
<tr><td>21</td><td>CAP2-</td><td>I</td><td>Capacitor 2 negative connection pin for voltage converter</td></tr>
<tr><td>22</td><td>V1</td><td>I</td><td rowspan="6">LCD driver supply voltages
Voltages should have the following relationship:
V0≧V1≧V2≧V3≧V4≧VSS</td></tr>
<tr><td>23</td><td>V2</td><td>I</td></tr>
<tr><td>24</td><td>V3</td><td>I</td></tr>
<tr><td>25</td><td>V4</td><td>I</td></tr>
<tr><td>26</td><td>V0</td><td>I</td></tr>
<tr><td>27</td><td>VR</td><td>I</td><td>V0 voltage adjustment pin</td></tr>
<tr><td>28</td><td>C86</td><td>I</td><td>Microprocessor interface select input pin in parallel mode
-C86=H:6800-series MPU interface
-C86=L:8080-series MPU interface</td></tr>
<tr><td>29</td><td>PS</td><td>I</td><td>Parallel/Series data input select input
<table>
<tr><th>PS</th><th>Interface Mode</th><th>Chip Select</th><th>Data/ Instruction</th><th>Data</th><th>Read/Write</th><th>Serial clock</th></tr>
<tr><td>H</td><td>Parallel</td><td>CS1B
CS2</td><td>RS</td><td>DB0 to DB7</td><td>E_RDB
RW_WRB</td><td>-</td></tr>
<tr><td>L</td><td>Serial</td><td>CS1B</td><td>RS</td><td>SID(DB7)</td><td>Write only</td><td>SCLK(DB6))</td></tr>
</table></td></tr>
</table>

			CS2 *Note:In serial mode,it is impossible to read data from the on-chip RAM.And DB0 to DB5 are high impedance and E_RDB and RW_WRB must be fixed to either “H”or “L”.
30	IRS	I	Internal resistor select pin This pin selects the resistors for adjusting V0 voltage level and is valid only in master operation. -IRS=”H”:use the internal resistors -IRS=”L”:use the external resistors

8. OUTLINE DIMENSIONS



9. RELIABILITY

Content of Reliability Test

Environmental Test				
No.	**Test Item**	**Content of Test**	**Test Condition**	**Applicable Standard**
1	High	Endurance test applying the high storage temperature	80 ℃	

	temperature storage	for a long time.	200 hrs	
2	Low temperature storage	Endurance test applying the low storage temperature for a long time.	-30 ℃ 200 hrs	
3	High temperature operation	Endurance test applying the electric stress (Voltage & Current) and the thermal stress to the element for a long time.	70 ℃ 200 hrs	
4	Low temperature operation	Endurance test applying the electric stress under low temperature for a long time.	-20 ℃ 200 hrs	
5	High temperature Humidity storage	Endurance test applying the high temperature and high humidity storage for a long time.	50 ℃ , 90 RH 96 hrs	MIL-202E-103B JIS-C5023
6	High temperature Humidity operation	Endurance test applying the electric stress (Voltage & Current) and temperature humidity stress to the element for a long time.	50 ℃ , 90 RH 96 hrs	MIL-202E-103B JIS-C5023
7	Temperature cycle	Endurance test applying the low and high temperature cycle. -20°C 30min. ⇌ 25°C 5min. ⇌ 70°C 30min. 1 cycle	-20℃ – 70℃ 10 cycles	
Mechanical Test				
8	Vibration test	Endurance test applying the vibration during transportation and using.	10-22Hz → 1.5mmp-p 22-500Hz → 1.5G Total 0.5hrs	MIL-202E-201A JIS-C5025 JIS-C7022-A-10
9	Shock test	Constructional and mechanical endurance test applying the shock during transportation.	50G half sign wave 1l msedc 3 times of each direction	MIL-202E-213B
10	Atmospheric pressure test	Endurance test applying the atmospheric pressure during transportation by air.	115 mbar 40 hrs	MIL-202E-105C
Others				
11	Static electricity test	Endurance test applying the electric stress to the terminal.	VS=800V, RS=1.5 k CS=100 pF 1 time	MIL-883B-3015.1

*** Supply voltage for logic system = 3V. Supply voltage for LCD system = Operating voltage at 25℃.

Failure Judgment Criterion

Criterion Item	Test Item No.											Failure Judgment Criterion
	1	2	3	4	5	6	7	8	9	10	11	
Basic specification												Out of the Basic Specification
Electrical characteristic												Out of the DC and AC Characteristic
Mechanical characteristic												Out of the Mechanical Specification Color change : Out of Limit Apperance Specification
Optical characteristic												Out of the Apperance Standard

10. QUALITY GUARANTEE

Acceptable Quality Level

Each lot should satisfy the quality level defined as follows.

- Inspection method : MIL-STD-105E LEVEL II Normal one time sampling
- AQL

Partition	AQL	Definition
A: Major	0.4%	Functional defective as product
B: Minor	1.5%	Satisfy all functions as product but not satisfy cosmetic standard

Definition of 'LOT'

One lot means the delivery quantity to customer at one time.

Conditions of Cosmetic Inspection

Environmental condition

The inspection should be performed at the 1cm of height from the LCD module under 2 pieces of 40W white fluorescent lamps (Normal temperature 20~25℃and normal humidity 60 ± 15%RH).

Inspection method

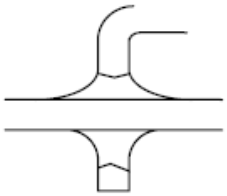
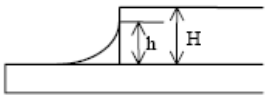
The visual check should be performed vertically at more than 30cm distance from the LCD panel.

Driving voltage

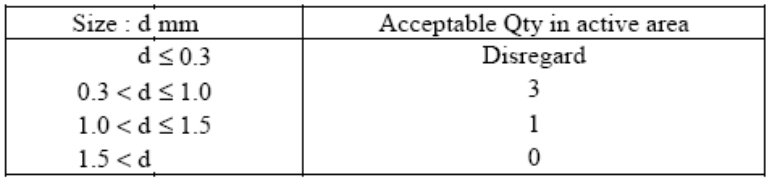
The VO value which the most optimal contrast can be obtained near the specified VO in the specification. (Within ± 0.5V of typical value at 25℃.).

11. INSPECTION CRITERIA

11.1 Module Cosmetic Criteria

No.	Item	Judgment Criterion	Partition
1	Difference in Spec.	None allowed	Major
2	Pattern peeling	No substrate pattern peeling and floating	Major
3	Soldering defects	No soldering missing No soldering bridge No cold soldering	Major Major Major
4	Resist flaw on substrate	Invisible copper foil (′0.5mm or more) on substrate pattern	Minor
5	Accretion of metallic Foreign matter	No soldering dust No accretion of metallic foreign matters (Not exceed ′0.2mm)	Minor Minor
6	Stain	No stain to spoil cosmetic badly	Minor
7	Plate discoloring	No plate fading, rusting and discoloring	Minor
8	Solder amount 1. Lead parts	a. Soldering side of PCB Solder to form a 'Filet' all around the lead. Solder should not hide the lead form perfectly. (too much) b. Components side (In case of 'Through Hole PCB') Solder to reach the Components side of PCB. 	Minor
	2. Flat packages	Either 'Toe' (A) or 'Seal' (B) of the lead to be covered by 'Filet'. Lead form to be assume over solder. A B 	Minor
	3. Chips	(3/2) H ≥h ≥(1/2) H 	Minor

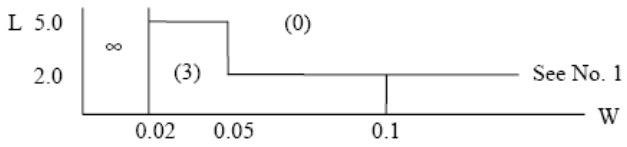
11.2 Screen Cosmetic Criteria (Non-Operating)

No.	Defect	Judgment Criterion	Partition
1	Spots	In accordance with *Screen Cosmetic Criteria (Operating) No.1*.	Minor
2	Lines	In accordance with *Screen Cosmetic Criteria (Operating) No.2*.	Minor
3	Bubbles in polarizer		Minor

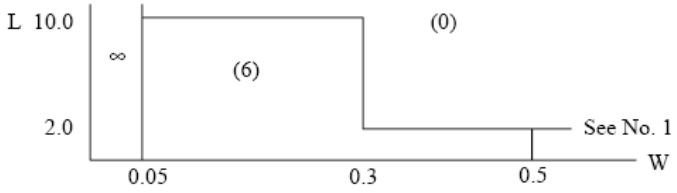
Size : d mm	Acceptable Qty in active area
d ≤ 0.3	Disregard
0.3 < d ≤ 1.0	3
1.0 < d ≤ 1.5	1
1.5 < d	0

4	Scratch	In accordance with spots and lines operating cosmetic criteria. When the light reflects on the panel surface, the scratches are not to be remarkable.	Minor
5	Allowable density	Above defects should be separated more than 30mm each other.	Minor
6	Coloration	Not to be noticeable coloration in the viewing area of the LCD panels. Back-lit type should be judged with back-lit on state only.	Minor
7	Contamination	Not to be noticeable.	Minor

11.3. Screen Cosmetic Criteria (Operating)

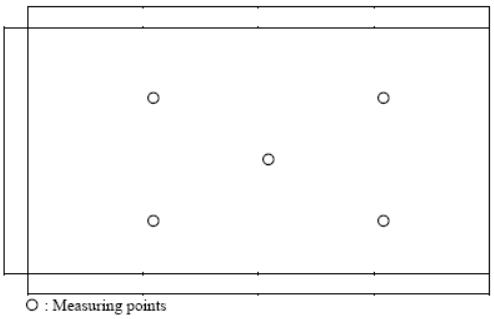
<table>
<tr><th>No.</th><th>Defect</th><th>Judgment Criterion</th><th>Partition</th></tr>
<tr><td>1</td><td>Spots</td><td>A) Clear Note :
<table><tr><th>Size : d mm</th><th>Acceptable Qty in active area</th></tr><tr><td>d ≤ 0.1</td><td>Disregard</td></tr><tr><td>0.1 < d ≤ 0.2</td><td>3</td></tr><tr><td>0.2 < d ≤ 0.3</td><td>2</td></tr><tr><td>0.3 < d</td><td>0</td></tr></table>Including pin holes and defective dots which must be within one pixel size.
B) Unclear Size :
<table><tr><th>Size : d mm</th><th>Acceptable Qty in active area</th></tr><tr><td>d ≤ 0.2</td><td>Disregard</td></tr><tr><td>0.2 < d ≤ 0.5</td><td>6</td></tr><tr><td>0.5 < d ≤ 0.7</td><td>2</td></tr><tr><td>0.7 < d</td><td>0</td></tr></table></td><td>Minor</td></tr>
<tr><td>2</td><td>Lines</td><td>A)Clear
 

Note : ( ) - Acceptable Qty in active area L -Length (mm) W - Width (mm)
∞ - Disregard

B) Unclear
 
</td><td>Minor</td></tr>
</table>

'Clear' = The shade and size are not changed by VO.
'Unclear' = The shade and size are changed by VO.

11.4. Screen Cosmetic Criteria (Operating) (Continued)

No.	Defect	Judgment Criterion	Partition
3	Rubbing line	Not to be noticeable.	
4	Allowable density	Above defects should be separated more than 10mm each other.	Minor
5	Rainbow	Not to be noticeable.	Minor
6	Dot size	To be 95% ~ 105% of the dot size (Typ.) in drawing. Partial defects of each dot (ex. pin-hole) should be treated as 'Spot'. (see *Screen Cosmetic Criteria (Operating) No.1*)	Minor
7	Uneven brightness (only back-lit type module)	Uneven brightness must be BMAX / BMIN ≤ 2 - BMAX : Max. value by measure in 5 points - BMIN : Min. value by measure in 5 points Divide active area into 4 vertically and horizontally. Measure 5 points shown in the following figure.  ○ : Measuring points	Minor

Note :

(1) Size : d = (long length + short length) / 2

(2) The limit samples for each item have priority.

(3) Complexed defects are defined item by item, but if the number of defects are defined in above table, the total number should not exceed 10.

(4) In case of 'concentration', even the spots or the lines of 'disregarded' size should not allowed. Following three situations should be treated as 'concentration'.

- 7 or over defects in circle of ′5mm.
- 10 or over defects in circle of ′10mm.
- 20 or over defects in circle of ′20mm.

12. PRECAUTIONS FOR USING LCD MODULES

Handing Precautions

(1) The display panel is made of glass. Do not subject it to a mechanical shock by dropping it or impact.

(2) If the display panel is damaged and the liquid crystal substance leaks out, be sure not to get any in your mouth. If the substance contacts your skin or clothes, wash it off using soap and water.

(3) Do not apply excessive force to the display surface or the adjoining areas since this may cause the

color tone to vary.

(4) The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.

(5) If the display surface becomes contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If it is heavily contaminated, moisten cloth with one of the following solvents :

- Isopropyl alcohol
- Ethyl alcohol

(6) Solvents other than those above-mentioned may damage the polarizer. Especially, do not use the following.

- Water
- Ketone
- Aromatic solvents

(7) Exercise care to minimize corrosion of the electrode. Corrosion of the electrodes is accelerated by water droplets, moisture condensation or a current flow in a high-humidity environment.

(8) Install the LCD Module by using the mounting holes. When mounting the LCD module make sure it is free of twisting, warping and distortion. In particular, do not forcibly pull or bend the I/O cable or the backlight cable.

(9) Do not attempt to disassemble or process the LCD module.

(10) NC terminal should be open. Do not connect anything.

(11) If the logic circuit power is off, do not apply the input signals.

(12) To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.

- Be sure to ground the body when handling the LCD modules.
- Tools required for assembling, such as soldering irons, must be properly grounded.
- To reduce the amount of static electricity generated, do not conduct assembling and other work under dry conditions.
- The LCD module is coated with a film to protect the display surface. Exercise care when peeling off this protective film since static electricity may be generated.

Storage Precautions

When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps. Keep the modules in bags (avoid high temperature, high humidity and low temperatures below 0□C). Whenever possible, the LCD modules should be stored in the same conditions in which they were shipped from our company.

Others

Liquid crystals solidify under low temperature (below the storage temperature range) leading to defective orientation or the generation of air bubbles (black or white). Air bubbles may also be generated if the module is subject to a low temperature.

If the LCD modules have been operating for a long time showing the same display patterns, the display patterns may remain on the screen as ghost images and a slight contrast irregularity may also appear. A normal operating status can be regained by suspending use for some time. It should be noted that this phenomenon does not adversely affect performance reliability.

To minimize the performance degradation of the LCD modules resulting from destruction caused by static electricity etc., exercise care to avoid holding the following sections when handling the modules.

- Exposed area of the printed circuit board.
- Terminal electrode sections.

13. USING LCD MODULES

Liquid Crystal Display Modules

LCD is composed of glass and polarizer. Pay attention to the following items when handling.

(1) Please keep the temperature within specified range for use and storage. Polarization degradation, bubble generation or polarizer peel-off may occur with high temperature and high humidity.

(2) Do not touch, push or rub the exposed polarizers with anything harder than an HB pencil lead (glass, tweezers, etc.).

(3) N-hexane is recommended for cleaning the adhesives used to attach front/rear polarizers and reflectors made of organic substances which will be damaged by chemicals such as acetone, toluene, ethanol and isopropylalcohol.

(4) When the display surface becomes dusty, wipe gently with absorbent cotton or other soft material like chamois soaked in petroleum benzin. Do not scrub hard to avoid damaging the display surface.

(5) Wipe off saliva or water drops immediately, contact with water over a long period of time may cause deformation or color fading.

(6) Avoid contacting oil and fats.

(7) Condensation on the surface and contact with terminals due to cold will damage, stain or dirty the polarizers. After products are tested at low temperature they must be warmed up in a container before coming is contacting with room temp erature air.

(8) Do not put or attach anything on the display area to avoid leaving marks on.

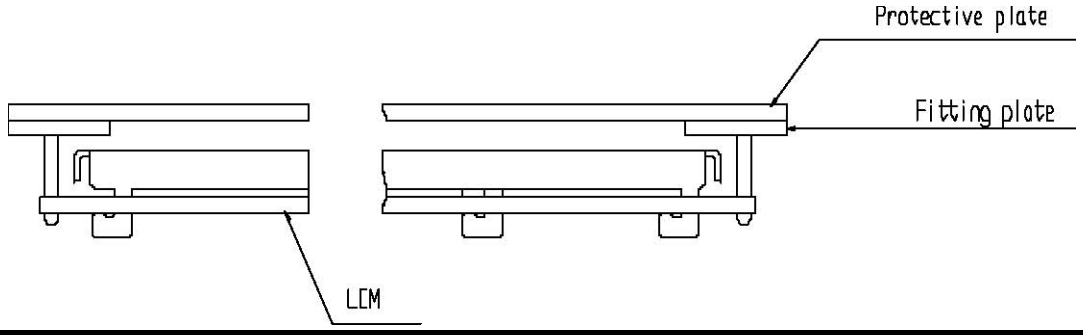
(9) Do not touch the display with bare hands. This will stain the display area and degradate insulation between terminals (some cosmetics are determinated to the polarizers).

(10) As glass is fragile. It tends to become or chipped during handling especially on the edges. Please avoid dropping or jarring.

Installing LCD Modules

The hole in the printed circuit board is used to fix LCM as shown in the picture below. Attend to the following items when installing the LCM.

(1) Cover the surface with a transparent protective plate to protect the polarizer and LC cell.



(2) When assembling the LCM into other equipment, the spacer to the bit between the LCM and the fitting plate should have enough height to avoid causing stress to the module surface, refer to the individual specifications for measurements. The measurement tolerance should be 0.1mm.

Precaution for Handing LCD Modules

Since LCM has been assembled and adjusted with a high degree of precision, avoid applying excessive shocks to the module or making any alterations or modifications to it.

(1) Do not alter, modify or change the the shape of the tab on the metal frame.

(2) Do not make extra holes on the printed circuit board, modify its shape or change the positions of components to be attached.

(3) Do not damage or modify the pattern writing on the printed circuit board.

(4) Absolutely do not modify the zebra rubber strip (conductive rubber) or heat seal connector.

(5) Except for soldering the interface, do not make any alterations or modifications with a soldering iron.

(6) Do not drop, bend or twist LCM.

Electro-Static Discharge Control

Since this module uses a CMOS LSI, the same careful attention should be paid to electrostatic discharge as for an ordinary CMOS IC.

(1) Make certain that you are grounded when handing LCM.

(2) Before remove LCM from its packing case or incorporating it into a set, be sure the module and your body have the same electric potential.

(3) When soldering the terminal of LCM, make certain the AC power source for the soldering iron does not leak.

(4) When using an electric screwdriver to attach LCM, the screwdriver should be of ground potentiality to minimize as much as possible any transmission of electromagnetic waves produced sparks coming from the commutator of the motor.

(5) As far as possible make the electric potential of your work clothes and that of the work bench the ground potential.

(6) To reduce the generation of static electricity be careful that the air in the work is not too dried. A relative humidity of 50%60% is recommended.

Precaution for soldering to the LCM

(1) Observe the following when soldering lead wire, connector cable and etc. to the LCM.

- Soldering iron temperature : 280□C 10□C.
- Soldering time : 3-4 sec.
- Solder : eutectic solder.

If soldering flux is used, be sure to remove any remaining flux after finishing to soldering operation. (This does not apply in the case of a non-halogen type of flux.) It is recommended that you protect the LCD surface with a cover during soldering to prevent any damage dur to flux spatters.

(2) When soldering the electroluminescent panel and PC board, the panel and board should not be detached more than three times. This maximum number is determined by the temperature and time conditions mentioned above, though there may be some variance depending on the temperature of the soldering iron.

(3) When remove the electoluminescent panel from the PC board, be sure the solder has completely melted, the soldered pad on the PC board could be damaged.

Precautions for Operation

(1) Viewing angle varies with the change of liquid crystal driving voltage (VO). Adjust VO to show the best contrast.

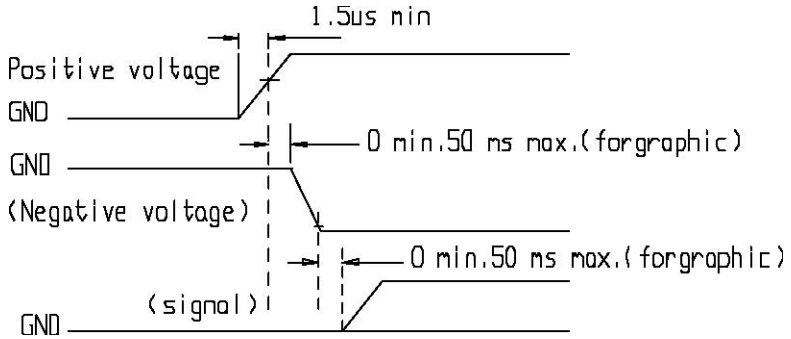
(2) Driving the LCD in the voltage above the limit shortens its life.

(3) Response time is greatly delayed at temperature below the operating temperature range. However, this does not mean the LCD will be out of the order. It will recover when it returns to the specified temperature range.

(4) If the display area is pushed hard during operation, the display will become abnormal. However, it will return to normal if it is turned off and then back on.

(5) Condensation on terminals can cause an electrochemical reaction disrupting the terminal circuit. Therefore, it must be used under the relative condition of 40□C , 50% RH.

(6) When turning the power on, input each signal after the positive/negative voltage becomes stable.



Storage

When storing LCDs as spares for some years, the following precaution are necessary.

(1) Store them in a sealed polyethylene bag. If properly sealed, there is no need for dessicant.

(2) Store them in a dark place. Do not expose to sunlight or fluorescent light, keep the temperature between 0□C and 35□C.

(3) The polarizer surface should not come in contact with any other objects. (We advise you to store them in the container in which they were shipped.)

Safety

(1) It is recommended to crush damaged or unnecessary LCDs into pieces and wash them off with solvents such as acetone and ethanol, which should later be burned.

(2) If any liquid leakes out of a damaged glass cell and comes in contact with the hands, wash off thoroughly with soap and water.

Limited Warranty

Unless agreed between YAOYU and customer, YAOYU will replace or repair any of its LCD modules which are found to be functionally defective when inspected in accordance with YAOYU LCD acceptance standards (copies available upon request) for a period of one year from date of shipments. Cosmetic/visual defects must be returned to YAOYU within 90 days of shipment. Confirmation of such date shall be based on freight documents. The warranty liability of YAOYU limited to repair and/or replacement on the terms set forth above. YAOYU will not be responsible for any subsequent or consequential events.

Return LCM under warranty

No warranty can be granted if the precautions stated above have been disregarded. The typical examples of violations are :

- Broken LCD glass.
- PCB eyelet's damaged or modified.
- PCB conductors damaged.
- Circuit modified in any way, including addition of components.
- PCB tampered with by grinding, engraving or painting varnish.
- soldering to or modifying the bezel in any manner.

Module repairs will be invoiced to the customer upon mutual agreement. Modules must be returned with sufficient description of the failures or defects. Any connectors or cable installed by the customer must be removed completely without damaging the PCB eyelet's, conductors and terminals.