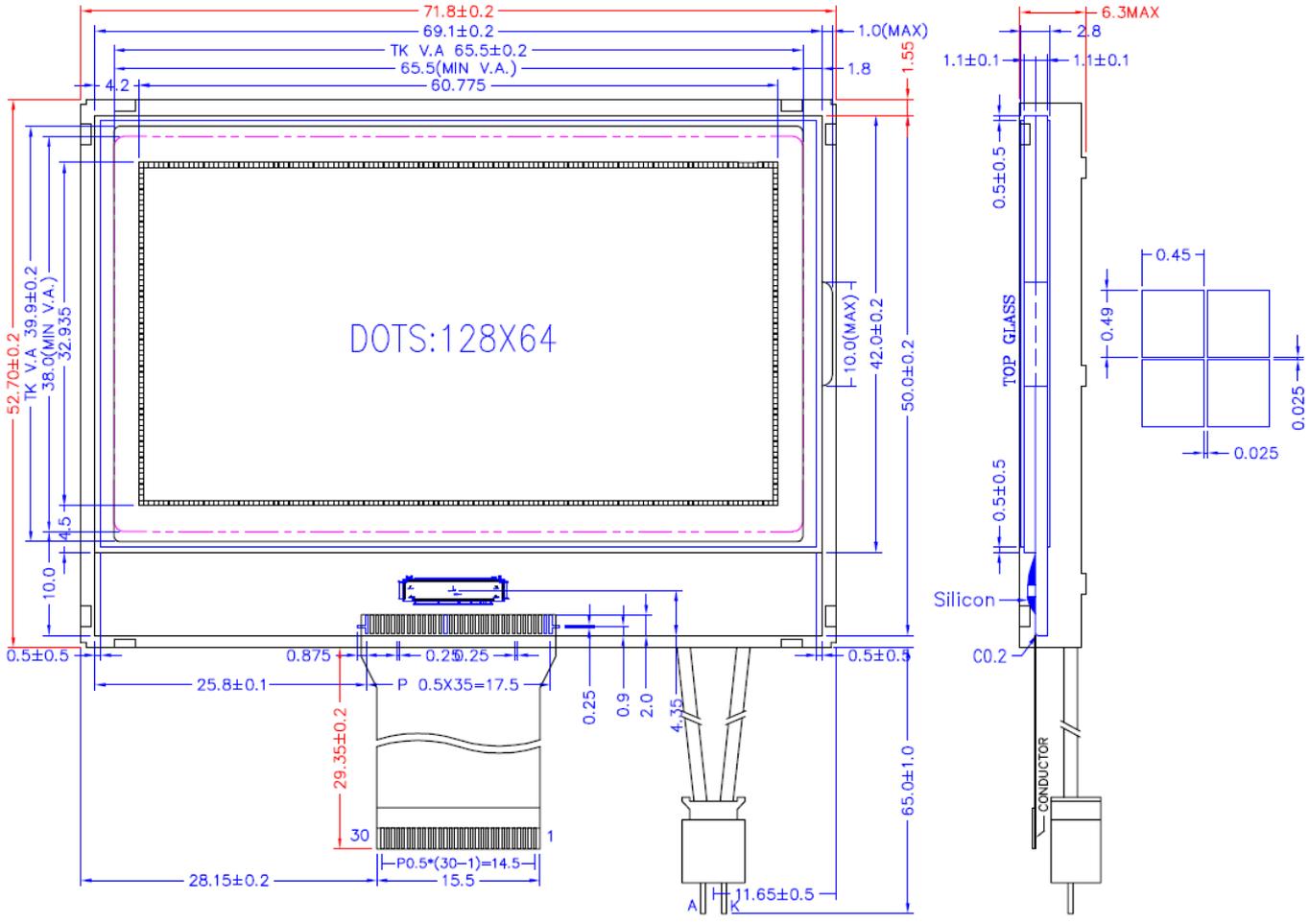


			CS2				
<p>*Note:In serial mode,it is impossible to read data from the on-chip RAM.And DB0 to DB5 are high impedance and E_RDB and RW_WRB must be fixed to either "H" or "L".</p>							
30	IRS	I	<p>Internal resistor select pin                  This pin selects the resistors for adjusting V0 voltage level and is valid only in master operation.                  -IRS="H":use the internal resistors                  -IRS="L":use the external resistors</p>				

8. OUTLINE DIMENSIONS



9. RELIABILITY

Content of Reliability Test

Environmental Test				
No.	Test Item	Content of Test	Test Condition	Applicable Standard
1	High	Endurance test applying the high storage temperature	80 °C	